

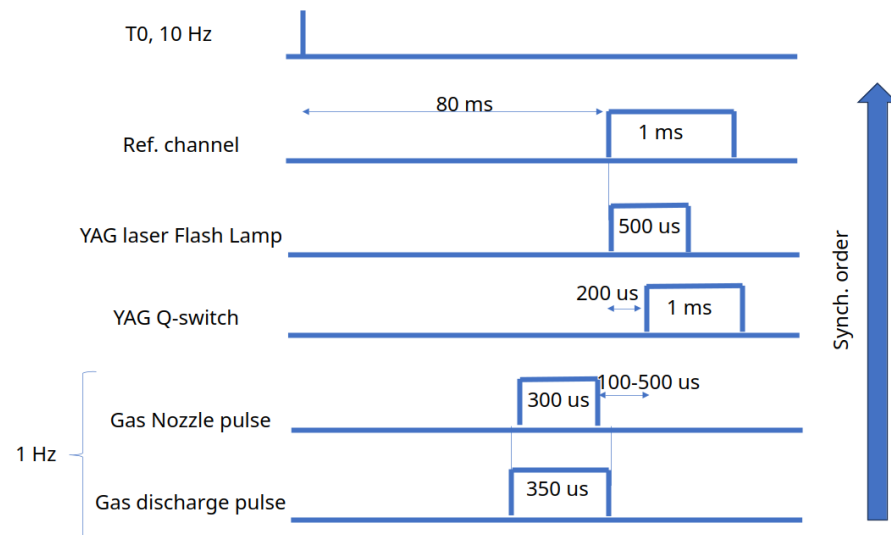
Sequencer for HAICU

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9 Feb 2026

Requirements

Source sequence



Other sequence signals

- MLD 1-4 (separate?)
- EM bender
- "slow" trap magnets (quad, top gate, booster)
- Bottom gate
- DAQ trigger(s?)

Requirements

- Synchronize to laser trigger pulse (10Hz-50Hz)
- Produce sequence of pulses over hundreds of ms or more
- ~10 channels
- Accuracy/synchronization of order microseconds
- Gate inputs to prevent triggering partial system

PPG capabilities

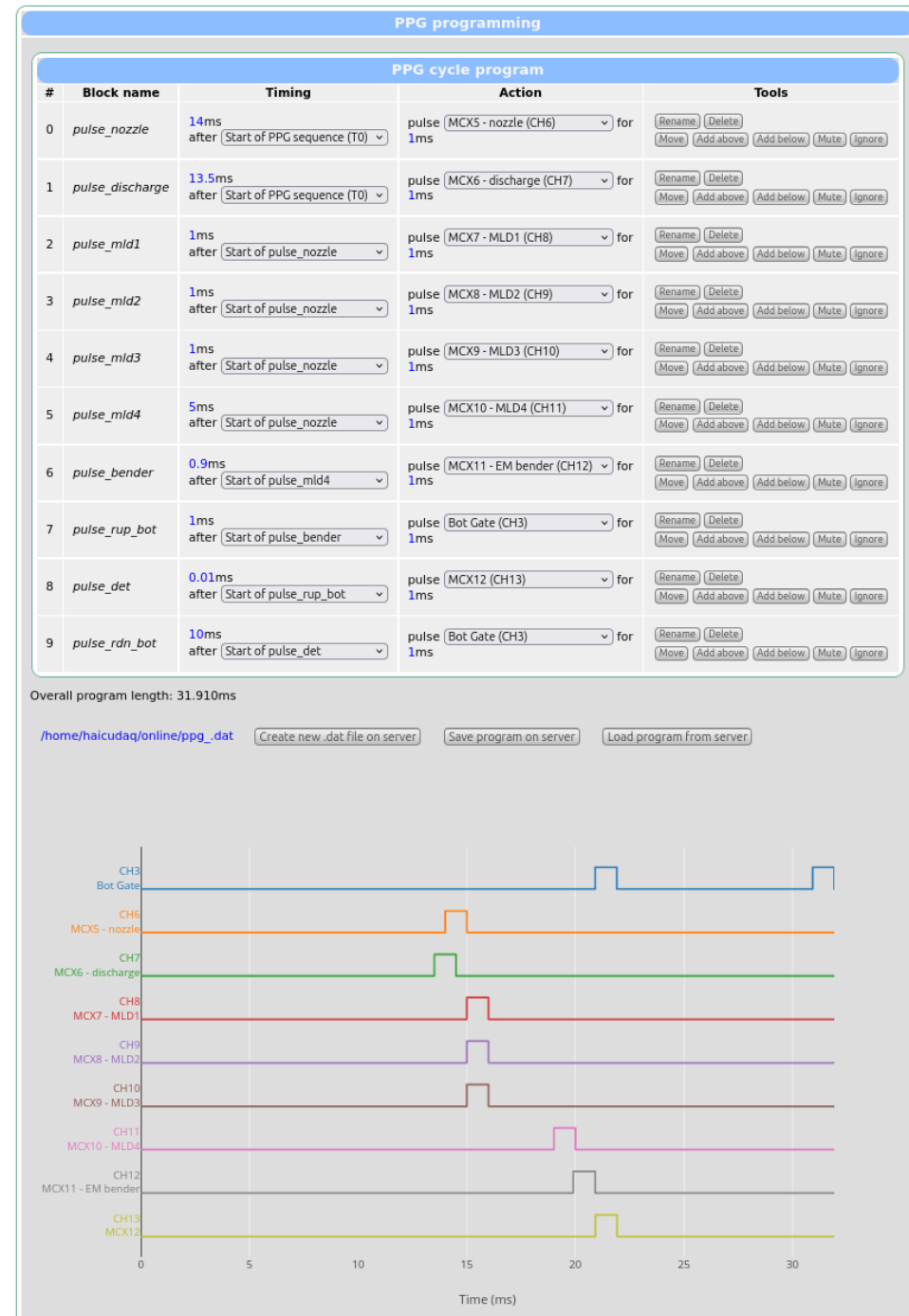
- FPGA based **P**rogrammable **P**ulse **G**enerator
- 100MHz clock, i.e. 10ns resolution
- Up to 32 independent outputs
- Up to 16 gate inputs and 16 veto inputs
- External trigger input
- Inputs and outputs can be freely mapped to ribbon connectors on back panel or coax connectors on front panel
- Coax in-/outputs can be switched between 3.3V and 5V

Design modified from <https://daq00.triumf.ca/DaqWiki/index.php/VME-PPG32>

User interface

- Sequences saved and loaded as hard-to read text file
- But convenient compiler with web-interface exists

```
#Created by python PPG compiler
#Ins 0=halt 1=cont 2=loop 3=endloop
#Note: PC is decimal; bitpats,delay,ins/data are hex
#PC set bitpat clr bitpat      delay  ins/data
Num Instruction Lines = 016    # IDDDDD  set bitpattern: bits 31 to 0    loopname
000 0x00000000 0xffffffff 0x00000000 0x100000 # 00000000000000000000000000000000;
001 0x00000010 0xffffffffef 0x000f423d 0x100000 # 00000000000000000000000000000000;
002 0x00000011 0xffffffffee 0x0001869d 0x100000 # 00000000000000000000000000000001;
003 0x00000010 0xffffffffef 0x00061a7d 0x100000 # 00000000000000000000000000000000;
004 0x00000012 0xffffffffed 0x0001869d 0x100000 # 00000000000000000000000000000010;
005 0x00000010 0xffffffffef 0x0033e13d 0x100000 # 00000000000000000000000000000000;
006 0x00000000 0xffffffffff 0x00632e9d 0x100000 # 00000000000000000000000000000000;
007 0x00000004 0xfffffffffb 0x0001869d 0x100000 # 00000000000000000000000000000010;
008 0x00000000 0xffffffffff 0x017bf19d 0x100000 # 00000000000000000000000000000000;
009 0x00000004 0xfffffffffb 0x0001869d 0x100000 # 00000000000000000000000000000010;
010 0x00000000 0xffffffffff 0x001cfddd 0x100000 # 00000000000000000000000000000000;
011 0x00000002 0xfffffffffd 0x0001869d 0x100000 # 00000000000000000000000000000010;
012 0x00000000 0xffffffffff 0x001cfddd 0x100000 # 00000000000000000000000000000000;
013 0x00000001 0xfffffffffe 0x0001869d 0x100000 # 00000000000000000000000000000001;
014 0x00000000 0xffffffffff 0x00000000 0x100000 # 00000000000000000000000000000000;
015 0x00000000 0xffffffffff 0x00000000 0x000000 # 00000000000000000000000000000000;
```



Demonstration

- <https://haicudaq01.triumf.ca/?cmd=custom&page=PPG>

PPG programming

PPG cycle program

| # | Block name | Timing | Action | Tools |
|---|------------|---|-------------------------------------|--|
| 0 | pulse_1 | 0ms after Start of PPG sequence (T0) ▾ | pulse Top Gate (CH1) ▾ for 2ms | <div>Rename Delete</div> <div>Move Add above Add below Mute Ignore</div> |
| 1 | pulse_2 | 5ms after End of previous block ▾ | pulse Booster (CH2) ▾ for 2ms | <div>Rename Delete</div> <div>Move Add above Add below Mute Ignore</div> |
| 2 | pulse_3 | 7ms after Start of PPG sequence (T0) ▾ | pulse MCX5 - nozzle (CH6) ▾ for 1ms | <div>Rename Delete</div> <div>Move Add above Add below Mute Ignore</div> |

Overall program length: 9.000ms

</home/haicudaq/online/ppg/channelID16.dat>

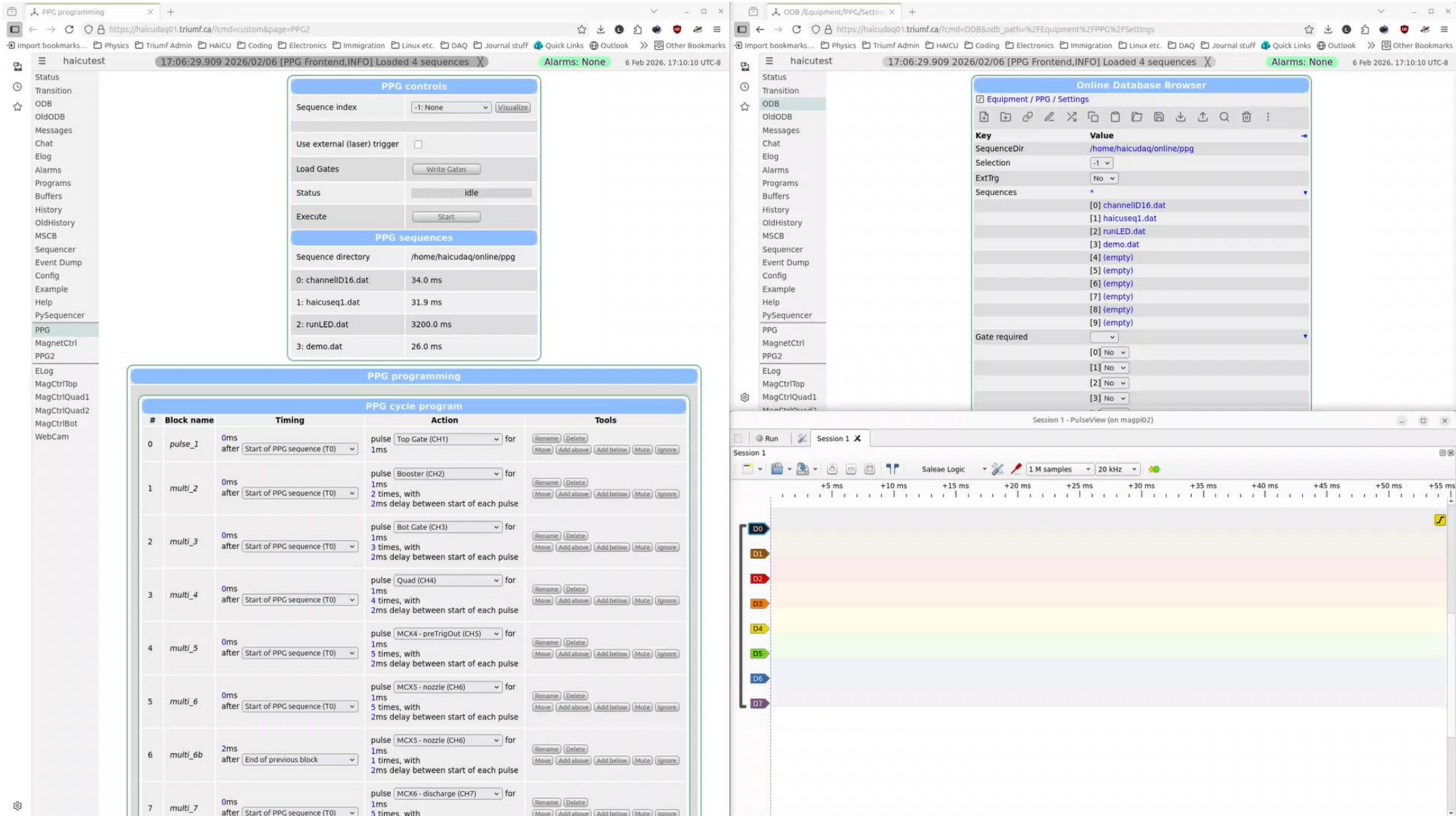
Create new .dat file on server

Save program on server

Load program from server

Channel names

| | | | | | | | |
|-----|-------------------|------|---------------|------|------------------|------|-------------------|
| CH1 | Top Gate | CH2 | Booster | CH3 | Bot Gate | CH4 | Quad |
| CH5 | MCX4 - preTrigOut | CH6 | MCX5 - nozzle | CH7 | MCX6 - discharge | CH8 | MCX7 - MLD1 |
| CH9 | MCX8 - MLD2 | CH10 | MCX9 - MLD3 | CH11 | MCX10 - MLD4 | CH12 | MCX11 - EM bender |



Future/Feedback

- Questions?
- Additional requirements?
- Problems or concerns?